



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: CYPR-CD00226

Inventor(s): Monte Mar and Warren Snyder

Serial No.: 09/929,891

Group Art Unit:

Filed: 08/14/01

Examiner:

Title: PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM
(AS AMENDED)

The Commissioner of Patents and Trademarks

Washington, D.C. 20231

Sir:

Information Disclosure Statement Submitted Pursuant to 37 C.F.R. 1.97(b)

The citations referenced herein, copies attached, may be material to the examination of the above-identified application and are, therefore, submitted in compliance with the duty of disclosure as defined in 37 C.F.R. 1.56. The Examiner is requested to make these citations of official record in the application.

This Information Disclosure Statement submitted in accordance with 37 C.F.R. 1.97(b) is not to be construed as a representation that a search has been made, that additional items material to the examination of this application do not exist, or that any one or more of these citations constitute prior art under 35 U.S.C. 102.

The Examiner's attention is respectfully directed to the following U.S. Patents:

| <u>Pat. No.</u> | <u>Pat. Title</u> | <u>Grant Date</u> |
|-----------------|--|-------------------|
| 6,144,327 | PROGRAMMABLY INTERCONNECTED PROGRAMMABLE DEVICES | 11/07/00 |
| 5,202,687 | ANALOG TO DIGITAL CONVERTER | 04/13/93 |

The Examiner's attention is respectfully directed to the following related pending U.S. patent applications:

CYPR-CD00169; "PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXEDANALOG/DIGITAL)"; 08/07/01; 09/924,734; Snyder et al.

CYPR-CD00173; "A PROGRAMMABLE ANALOG SYSTEM ARCHITECTURE"; 07/18/01; 09/909,047; M. Mar

CYPR-CD00174; "PROGRAMMING METHODOLOGY AND ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM"; 08/14/01; 09/930,021; Mar et al.


CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; Snyder

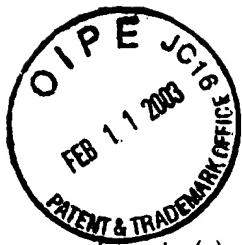
Please direct all correspondence concerning the above-identified application to the following address:

WAGNER, MURABITO & HAO LLP
Two North Market Street, Third Floor
San Jose, California 95113
(408) 938-9060

Respectfully submitted,

Date: 2/7/2003

By: 
Anthony C. Murabito
Reg. No. 35,295



Attorney Docket No.: CYPR-CD00226

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
Patent Application

Inventor(s): Monte Mar and Warren Snyder

Serial No.: 09/929,891

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Examiner:

Title: PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM
(AS AMENDED)

Form 1449

U.S. Patent Documents

| Examiner Initial | No. | Patent No. | Date | Patentee | Class | Sub-class | Filing Date |
|------------------|-----|------------|----------|-----------------|-------|-----------|-------------|
| | A | 6,144,327 | 11/07/00 | Distinti et al. | 341 | 126 | 08/12/97 |
| | B | 5,202,687 | 04/13/93 | Distinti | 341 | 158 | 06/12/91 |
| | C | | | | | | |
| | D | | | | | | |
| | E | | | | | | |

Foreign Patent or Published Foreign Patent Application

| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation | |
|------------------|-----|--------------|------------------|--------------------------|-------|-----------|-------------|----|
| | | | | | | | Yes | No |
| | F | | | | | | | |
| | G | | | | | | | |
| | H | | | | | | | |

Other Documents

| Examiner Initial | No. | Docket Number, Title, Filing Date, Serial Number & Inventors |
|------------------|-----|---|
| | I | CYPR-CD00169; "PROGRAMMABLE MICROCONTROLLER ARCHITECTURE (MIXEDANALOG/DIGITAL)"; 08/07/01; 09/924,734; Snyder et al. |
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| | | CYPR-CD00232; "PROGRAMMABLE SYSTEM ON A CHIP"; 10/01/01; 10/033,027; Snyder |
| Examiner | | Date Considered |

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.



2121
Patent # 71
02.26-03

Docket No.: CYPR-CD00226

Information Disclosure Statement Transmittal

| | | | |
|--|----------|---|--------------------------|
| I hereby certify that this transmittal of the below described document is being deposited with the United States Postal Service in an envelope bearing First Class Postage and addressed to the Commissioner of Patents and Trademarks, Washington, D.C., 20231, on the below date of deposit. | | | |
| Date of Deposit: | 02/07/03 | Name of Person Making the Deposit: | KATHERINE RINALDI |
| | | Signature of the Person Making the Deposit: | <i>Katherine Rinaldi</i> |

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Monte Mar and Warren Snyder

Serial No.: 09/929,891

Group Art Unit: 2121

Filed: 08/14/01

Examiner: not yet assigned

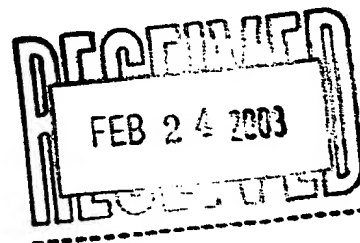
Title: PROGRAMMING ARCHITECTURE FOR A PROGRAMMABLE ANALOG SYSTEM
(AS AMENDED)

The Commissioner of Patents and Trademarks
Washington, D.C. 20231
Sir:

Information Disclosure Statement Transmittal

Transmitted herewith is the following:

- Formal drawings, totaling sheets.
- Informal drawings, totaling sheets.
- Certification for PTO Consideration
- ☒ Information Disclosure statement (2 sheets)
- Information Disclosure statement and late filing fee
- ☒ Form 1449
- Petition for Extension of Time
- ☒ Other: References
- ☒ Other: Related pending U.S. patent application



| F Calculation (for other than a small entity) | | | | | |
|---|--|--|--|----------|--------|
| Fee Items | | | | Fee Rate | Total |
| Petition for Extension of Time (fee calculated elsewhere) | | | | \$.00 | \$0.00 |
| Information Disclosure Statement, late filing | | | | \$180.00 | \$0.00 |
| Other: | | | | | \$0.00 |
| Total Fees | | | | | \$0.00 |

PAYMENT OF FEES


1. The full fee due in connection with this communication is provided as follows:
 - ☒ The Commissioner is hereby authorized to charge any additional fees associated with this communication or credit any overpayment to Deposit Account No.: 23-0085.
A duplicate copy of this authorization is enclosed.
 - ☐ A check in the amount of \$
 - ☐ Charge any fees required or credit any overpayments associated with this filing to Deposit Account No.: 23-0085.

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